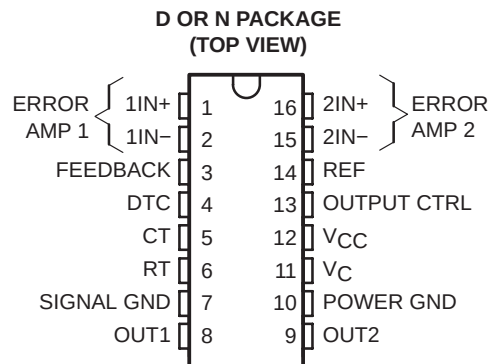


- Complete PWM Power-Control Function
- Totem-Pole Outputs for 200-mA Sink or Source Current
- Output Control Selects Parallel or Push-Pull Operation
- Internal Circuitry Prohibits Double Pulse at Either Output
- Variable Dead-Time Provides Control Over Total Range
- Internal Regulator Provides a Stable 5-V Reference Supply, Trimmed to 1% Tolerance
- On-Board Output Current-Limiting Protection
- Undervoltage Lockout for Low- $V_{CC}$  Conditions
- Separate Power and Signal Grounds



### description/ordering information

The TL598 incorporates all the functions required in the construction of pulse-width-modulated (PWM) controlled systems on a single chip. Designed primarily for power-supply control, the TL598 provides the systems engineer with the flexibility to tailor the power-supply control circuits to a specific application.

The TL598 contains two error amplifiers, an internal oscillator (externally adjustable), a dead-time control (DTC) comparator, a pulse-steering flip-flop, a 5-V precision reference, undervoltage lockout control, and output control circuits. Two totem-pole outputs provide exceptional rise- and fall-time performance for power FET control. The outputs share a common source supply and common power ground terminals, which allow system designers to eliminate errors caused by high current-induced voltage drops and common-mode noise.

The error amplifier has a common-mode voltage range of 0 V to  $V_{CC} - 2$  V. The DTC comparator has a fixed offset that prevents overlap of the outputs during push-pull operation. A synchronous multiple supply operation can be achieved by connecting RT to the reference output and providing a sawtooth input to CT.

The TL598 device provides an output control function to select either push-pull or parallel operation. Circuit architecture prevents either output from being pulsed twice during push-pull operation. The output frequency for push-pull applications is one-half the oscillator frequency  $\left(f_o = \frac{1}{2 RT CT}\right)$ . For single-ended applications:

$$f_o = \frac{1}{RT CT}$$

### ORDERING INFORMATION

| $T_A$       | PACKAGE† |              | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
|-------------|----------|--------------|-----------------------|------------------|
| 0°C to 70°C | PDIP (N) | Tube of 25   | TL598CN               | TL598CN          |
|             | SOIC (D) | Tube of 40   | TL598CD               | TL598C           |
|             |          | Reel of 2500 | TL598CDR              |                  |

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).



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**TEXAS  
INSTRUMENTS**

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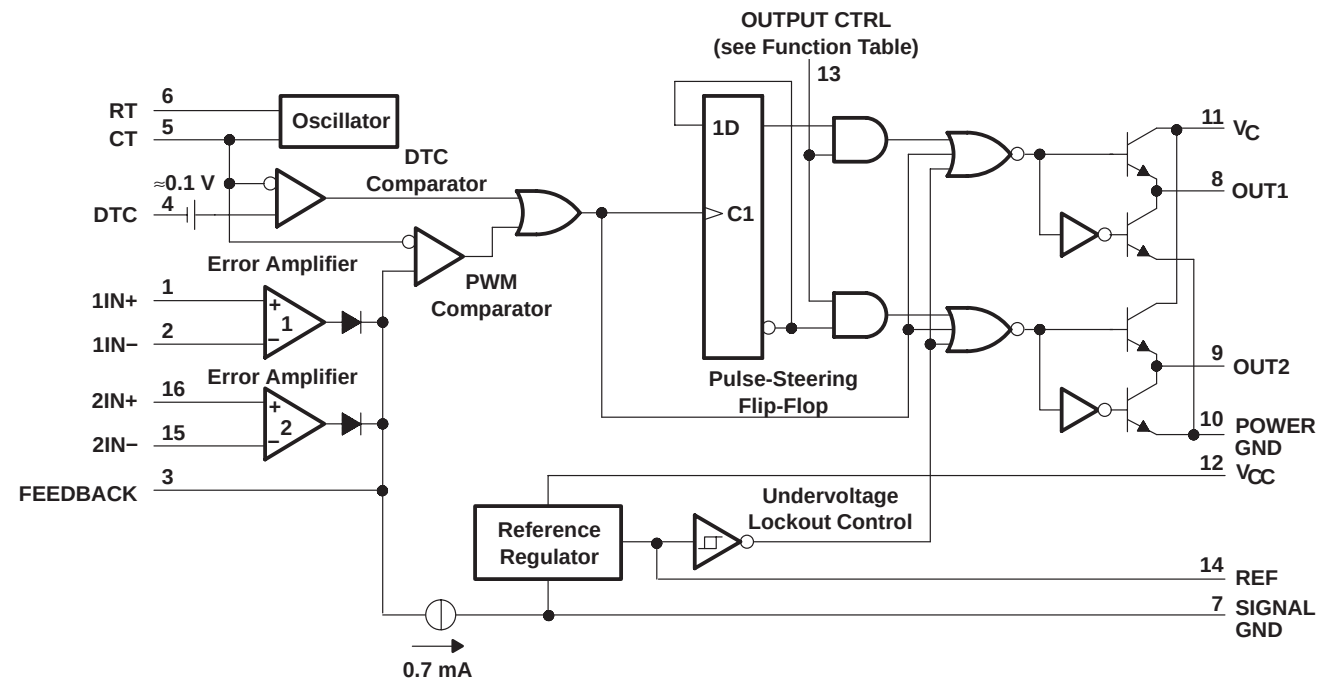
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# TL598 PULSE-WIDTH-MODULATION CONTROL CIRCUITS

SLVS053D – FEBRUARY 1988 – REVISED NOVEMBER 2003

| FUNCTION TABLE     |                                 |
|--------------------|---------------------------------|
| INPUT/OUTPUT CTRL  | OUTPUT FUNCTION                 |
| $V_I = \text{GND}$ | Single-ended or parallel output |
| $V_I = \text{REF}$ | Normal push-pull operation      |

## functional block diagram



## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

|                                                                         |                  |
|-------------------------------------------------------------------------|------------------|
| Supply voltage, $V_{CC}$ (see Note 1)                                   | 41 V             |
| Amplifier input voltage, $V_I$                                          | $V_{CC} + 0.3$ V |
| Collector voltage                                                       | 41 V             |
| Output current (each output), sink or source, $I_O$                     | 250 mA           |
| Package thermal impedance, $\theta_{JA}$ (see Notes 2 and 3): D package | 73°C/W           |
| N package                                                               | 67°C/W           |
| Operating virtual junction temperature, $T_J$                           | 150°C            |
| Storage temperature range, $T_{Stg}$                                    | -65°C to 150°C   |

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES:
1. All voltage values, except differential voltages, are with respect to the signal ground terminal.
  2. Maximum power dissipation is a function of  $T_J(\text{max})$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(\text{max}) - T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can impact reliability.
  3. The package thermal impedance is calculated in accordance with JESD 51-7.

## recommended operating conditions

|                                                              | MIN     | MAX                | UNIT |
|--------------------------------------------------------------|---------|--------------------|------|
| V <sub>CC</sub> Supply voltage                               | 7       | 40                 | V    |
| V <sub>I</sub> Amplifier input voltage                       | 0       | V <sub>CC</sub> –2 | V    |
| I <sub>O</sub> Collector voltage                             |         | 40                 | V    |
| I <sub>IL</sub> Output current (each output), sink or source |         | 200                | mA   |
| Current into feedback terminal                               |         | 0.3                | mA   |
| C <sub>T</sub> Timing capacitor                              | 0.00047 | 10                 | μF   |
| R <sub>T</sub> Timing resistor                               | 1.8     | 500                | kΩ   |
| f <sub>osc</sub> Oscillator frequency                        | 1       | 300                | kHz  |
| T <sub>A</sub> Operating free-air temperature                | 0       | 70                 | °C   |

## electrical characteristics over recommended operating free-air temperature range, V<sub>CC</sub> = 15 V (unless otherwise noted)

### reference section (see Note 4)

| PARAMETER                              | TEST CONDITIONS†               | MIN  | TYP‡ | MAX  | UNIT |
|----------------------------------------|--------------------------------|------|------|------|------|
| Output voltage (REF)                   | I <sub>O</sub> = 1 mA          | 4.95 | 5    | 5.05 | V    |
|                                        | T <sub>A</sub> = 25°C          |      |      |      |      |
|                                        | T <sub>A</sub> = full range    | 4.9  |      | 5.1  |      |
| Input regulation                       | V <sub>CC</sub> = 7 V to 40 V  |      | 2    | 25   | mV   |
| Output regulation                      | I <sub>O</sub> = 1 mA to 10 mA |      | 1    | 15   | mV   |
|                                        | T <sub>A</sub> = 25°C          |      |      |      |      |
|                                        | T <sub>A</sub> = full range    |      |      | 50   |      |
| Output voltage change with temperature | ΔT <sub>A</sub> = MIN to MAX   |      | 2    | 10   | mV/V |
| Short-circuit output current§          | REF = 0 V                      | –10  | –48  |      | mA   |

† Full range is 0°C to 70°C.

‡ All typical values, except for parameter changes with temperature, are at T<sub>A</sub> = 25°C.

§ Duration of the short circuit should not exceed one second.

NOTE 4: Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

### oscillator section, C<sub>T</sub> = 0.001 μF, R<sub>T</sub> = 12 kΩ (see Figure 1) (see Note 4)

| PARAMETER                          | TEST CONDITIONS†                                                                          | MIN | TYP‡ | MAX | UNIT   |
|------------------------------------|-------------------------------------------------------------------------------------------|-----|------|-----|--------|
| Frequency                          |                                                                                           |     | 100  |     | kHz    |
| Standard deviation of frequency¶   | All values of V <sub>CC</sub> , C <sub>T</sub> , R <sub>T</sub> , T <sub>A</sub> constant |     | 100  |     | Hz/kHz |
| Frequency change with voltage      | V <sub>CC</sub> = 7 V to 40 V, T <sub>A</sub> = 25°C                                      |     | 1    | 10  | Hz/kHz |
| Frequency change with temperature# | ΔT <sub>A</sub> = full range                                                              |     | 70   | 120 | Hz/kHz |
|                                    | ΔT <sub>A</sub> = full range, C <sub>T</sub> = 0.01 μF                                    |     | 50   | 80  |        |

† Full range is 0°C to 70°C.

‡ All typical values, except for parameter changes with temperature, are at T<sub>A</sub> = 25°C.

¶ Standard deviation is a measure of the statistical distribution about the mean, as derived from the formula:

$$\sigma = \sqrt{\frac{\sum_{n=1}^N (x_n - \bar{x})^2}{N - 1}}$$

# Effects of temperature on external R<sub>T</sub> and C<sub>T</sub> are not taken into account.

NOTE 4: Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

# TL598

## PULSE-WIDTH-MODULATION CONTROL CIRCUITS

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**electrical characteristics over recommended operating free-air temperature range,  $V_{CC} = 15\text{ V}$  (unless otherwise noted) (continued)**

**error amplifier section (see Note 4)**

| PARAMETER                        | TEST CONDITIONS                                                                    | MIN             | TYP <sup>†</sup> | MAX | UNIT |
|----------------------------------|------------------------------------------------------------------------------------|-----------------|------------------|-----|------|
| Input offset voltage             | FEEDBACK = 2.5 V                                                                   |                 | 2                | 10  | mV   |
| Input offset current             | FEEDBACK = 2.5 V                                                                   |                 | 25               | 250 | nA   |
| Input bias current               | FEEDBACK = 2.5 V                                                                   |                 | 0.2              | 1   | μA   |
| Common-mode input voltage range  | $V_{CC} = 7\text{ V to }40\text{ V}$                                               | 0 to $V_{CC}-2$ |                  |     | V    |
| Open-loop voltage amplification  | $\Delta V_O$ (FEEDBACK) = 3 V, $V_O$ (FEEDBACK) = 0.5 V to 3.5 V                   | 70              | 95               |     | dB   |
| Unity-gain bandwidth             |                                                                                    |                 | 800              |     | kHz  |
| Common-mode rejection ratio      | $V_{CC} = 40\text{ V}$ , $\Delta V_{IC} = 6.5\text{ V}$ , $T_A = 25^\circ\text{C}$ | 65              | 80               |     | dB   |
| Output sink current (FEEDBACK)   | FEEDBACK = 0.5 V                                                                   | 0.3             | 0.7              |     | mA   |
| Output source current (FEEDBACK) | FEEDBACK = 3.5 V                                                                   | -2              |                  |     | mA   |
| Phase margin at unity gain       | FEEDBACK = 0.5 V to 3.5 V, $R_L = 2\text{ k}\Omega$                                |                 | 65°              |     |      |
| Supply-voltage rejection ratio   | FEEDBACK = 2.5 V, $\Delta V_{CC} = 33\text{ V}$ , $R_L = 2\text{ k}\Omega$         |                 | 100              |     | dB   |

<sup>†</sup> All typical values, except for parameter changes with temperature, are at  $T_A = 25^\circ\text{C}$ .

NOTE 4. Pulse-testing techniques that maintain the junction temperature as close to the ambient temperature as possible must be used.

**electrical characteristics over recommended operating free-air temperature range,  $V_{CC} = 15\text{ V}$  (unless otherwise noted)**

**undervoltage lockout section (see Note 4)**

| PARAMETER               | TEST CONDITIONS <sup>‡</sup>     | MIN | MAX | UNIT |
|-------------------------|----------------------------------|-----|-----|------|
| Threshold voltage       | $T_A = 25^\circ\text{C}$         | 4   | 6   | V    |
|                         | $\Delta T_A = \text{full range}$ | 3.5 | 6.9 |      |
| Hysteresis <sup>§</sup> | $T_A = 25^\circ\text{C}$         | 100 |     | mV   |
|                         | $T_A = \text{full range}$        | 50  |     |      |

<sup>‡</sup> Full range is  $0^\circ\text{C}$  to  $70^\circ\text{C}$ .

<sup>§</sup> Hysteresis is the difference between the positive-going input threshold voltage and the negative-going input threshold voltage.

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

**output section (see Note 4)**

| PARAMETER                    | TEST CONDITIONS                                                       | MIN | MAX | UNIT |
|------------------------------|-----------------------------------------------------------------------|-----|-----|------|
| High-level output voltage    | $V_{CC} = 15\text{ V}$ , $V_C = 15\text{ V}$ , $I_O = -200\text{ mA}$ | 12  |     | V    |
|                              | $I_O = -20\text{ mA}$                                                 | 13  |     |      |
| Low-level output voltage     | $V_{CC} = 15\text{ V}$ , $V_C = 15\text{ V}$ , $I_O = 200\text{ mA}$  |     | 2   | V    |
|                              | $I_O = 20\text{ mA}$                                                  |     | 0.4 |      |
| Output-control input current | $V_I = V_{\text{ref}}$                                                |     | 3.5 | mA   |
|                              | $V_I = 0.4\text{ V}$                                                  |     | 100 | μA   |

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

**electrical characteristics over recommended operating free-air temperature range,  $V_{CC} = 15\text{ V}$  (unless otherwise noted) (continued)**

**dead-time control section (see Figure 1) (see Note 4)**

| PARAMETER                       | TEST CONDITIONS              | MIN  | TYP <sup>†</sup> | MAX | UNIT          |
|---------------------------------|------------------------------|------|------------------|-----|---------------|
| Input bias current (DTC)        | $V_I = 0$ to $5.25\text{ V}$ |      | -2               | -10 | $\mu\text{A}$ |
| Maximum duty cycle, each output | DTC = $0\text{ V}$           | 0.45 |                  |     |               |
| Input threshold voltage (DTC)   | Zero duty cycle              |      | 3                | 3.3 | V             |
|                                 | Maximum duty cycle           | 0    |                  |     |               |

<sup>†</sup> All typical values, except for parameter changes with temperature, are at  $T_A = 25^\circ\text{C}$ .

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

**pwm comparator section (see Note 4)**

| PARAMETER                          | TEST CONDITIONS                     | MIN | TYP <sup>†</sup> | MAX | UNIT |
|------------------------------------|-------------------------------------|-----|------------------|-----|------|
| Input threshold voltage (FEEDBACK) | DTC = $0\text{ V}$                  |     | 3.75             | 4.5 | V    |
| Input sink current (FEEDBACK)      | $V(\text{FEEDBACK}) = 0.5\text{ V}$ | 0.3 | 0.7              |     | mA   |

<sup>†</sup> All typical values, except for parameter changes with temperature, are at  $T_A = 25^\circ\text{C}$ .

NOTE Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

**total device (see Figure 1) (see Note 4)**

| PARAMETER              | TEST CONDITIONS                                              | MIN                    | TYP <sup>†</sup> | MAX | UNIT |
|------------------------|--------------------------------------------------------------|------------------------|------------------|-----|------|
| Standby supply current | RT = $V_{\text{ref}}$ ,<br>All other inputs and outputs open | $V_{CC} = 15\text{ V}$ | 15               | 21  | mA   |
|                        |                                                              | $V_{CC} = 40\text{ V}$ | 20               | 26  |      |
| Average supply current | DTC = $2\text{ V}$                                           |                        | 15               |     | mA   |

<sup>†</sup> All typical values, except for parameter changes with temperature, are at  $T_A = 25^\circ\text{C}$ .

NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

**switching characteristics,  $T_A = 25^\circ\text{C}$  (see Note 4)**

| PARAMETER                | TEST CONDITIONS               |            |             | MIN | TYP | MAX | UNIT |
|--------------------------|-------------------------------|------------|-------------|-----|-----|-----|------|
| Output-voltage rise time | CL = 1500 pF,<br>See Figure 2 | VC = 15 V, | VCC = 15 V, |     | 60  | 150 | ns   |
| Output-voltage fall time |                               |            |             |     | 35  | 75  |      |

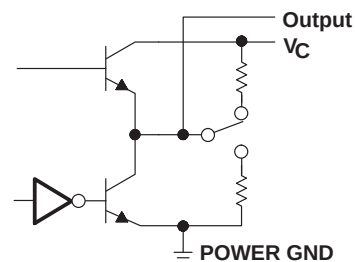
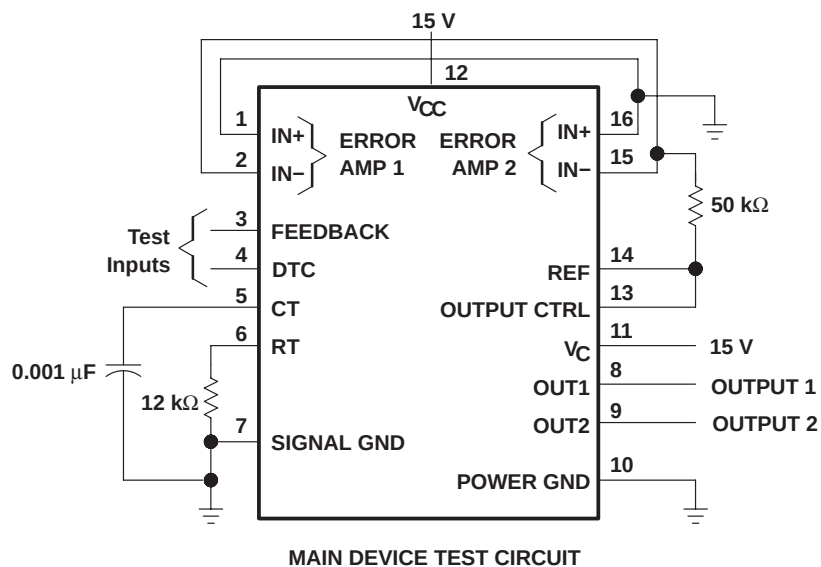
NOTE 4. Pulse-testing techniques must be used that maintain the junction temperature as close to the ambient temperature as possible.

# TL598

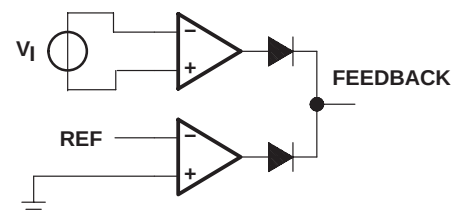
## PULSE-WIDTH-MODULATION CONTROL CIRCUITS

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### PARAMETER MEASUREMENT INFORMATION

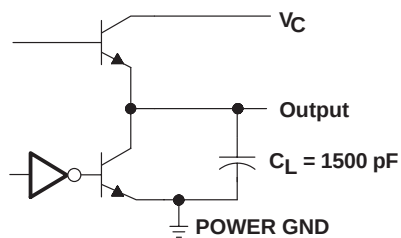


OUTPUT CONFIGURATION

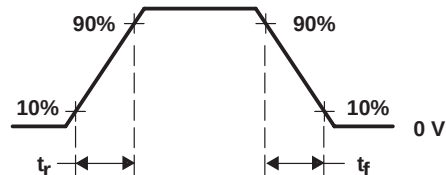


ERROR-AMPLIFIER TEST CIRCUIT

Figure 1. Test Circuits



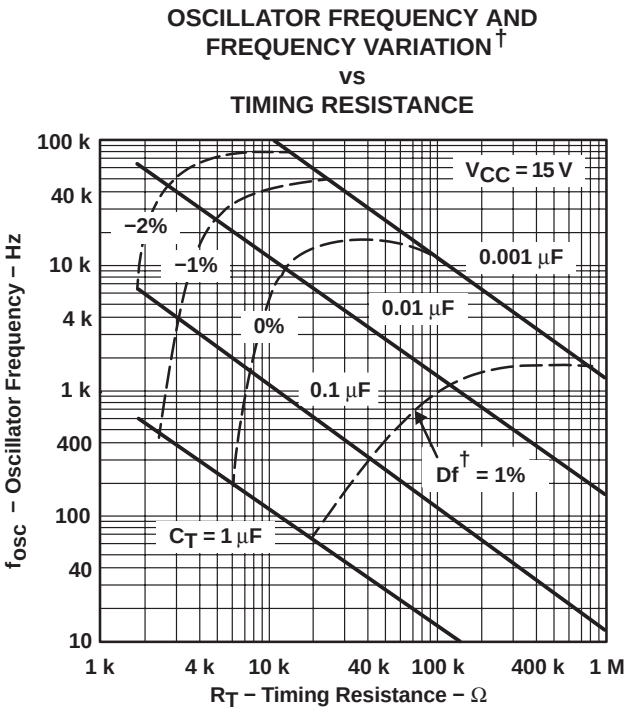
OUTPUT CONFIGURATION



OUTPUT-VOLTAGE WAVEFORM

Figure 2. Switching Output Configuration and Voltage Waveform

TYPICAL CHARACTERISTICS



<sup>†</sup> Frequency variation ( $\Delta f$ ) is the change in predicted oscillator frequency that occurs over the full temperature range.

Figure 3

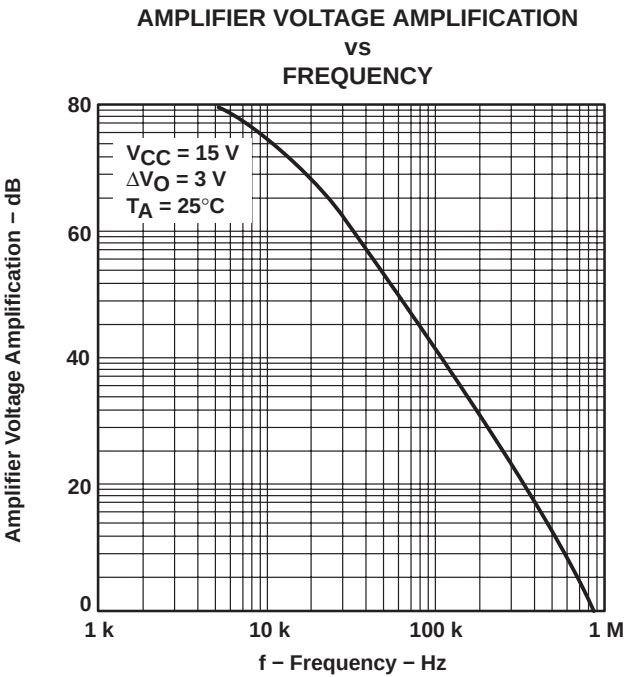


Figure 4

## PACKAGING INFORMATION

| Orderable part number    | Status<br>(1) | Material type<br>(2) | Package   Pins | Package qty   Carrier | RoHS<br>(3) | Lead finish/<br>Ball material<br>(4) | MSL rating/<br>Peak reflow<br>(5) | Op temp (°C) | Part marking<br>(6) |
|--------------------------|---------------|----------------------|----------------|-----------------------|-------------|--------------------------------------|-----------------------------------|--------------|---------------------|
| <a href="#">TL598CD</a>  | Obsolete      | Production           | SOIC (D)   16  | -                     | -           | Call TI                              | Call TI                           | 0 to 70      | TL598C              |
| <a href="#">TL598CDR</a> | NRND          | Production           | SOIC (D)   16  | 2500   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | 0 to 70      | TL598C              |
| TL598CDR.A               | NRND          | Production           | SOIC (D)   16  | 2500   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | 0 to 70      | TL598C              |
| <a href="#">TL598CN</a>  | NRND          | Production           | PDIP (N)   16  | 25   TUBE             | Yes         | NIPDAU                               | N/A for Pkg Type                  | 0 to 70      | TL598CN             |
| TL598CN.A                | NRND          | Production           | PDIP (N)   16  | 25   TUBE             | Yes         | NIPDAU                               | N/A for Pkg Type                  | 0 to 70      | TL598CN             |
| TL598CNE4                | NRND          | Production           | PDIP (N)   16  | 25   TUBE             | Yes         | NIPDAU                               | N/A for Pkg Type                  | 0 to 70      | TL598CN             |

<sup>(1)</sup> **Status:** For more details on status, see our [product life cycle](#).

<sup>(2)</sup> **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

<sup>(4)</sup> **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

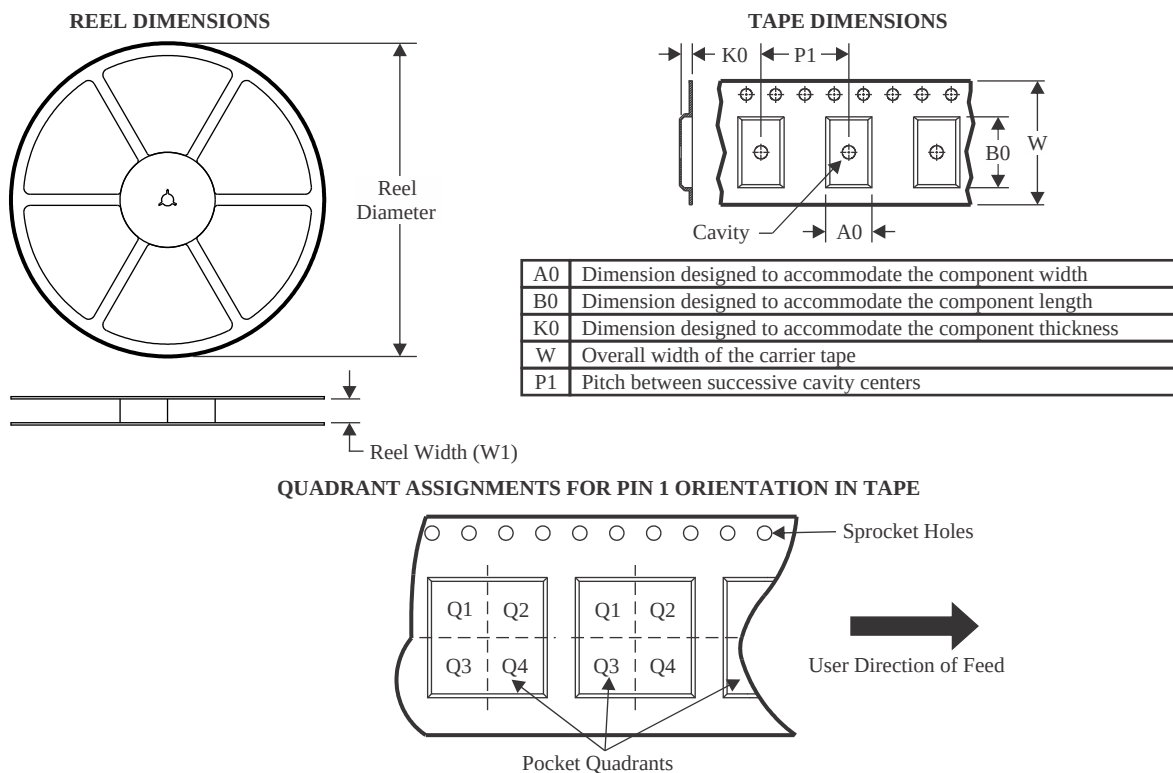
Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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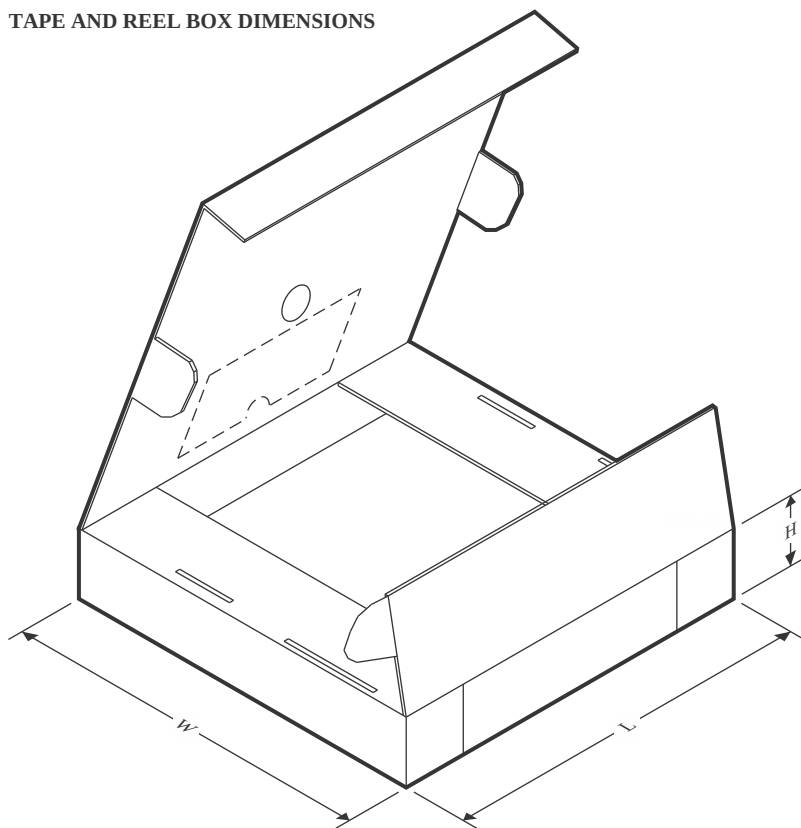
## TAPE AND REEL INFORMATION



\*All dimensions are nominal

| Device   | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|----------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| TL598CDR | SOIC         | D               | 16   | 2500 | 330.0              | 16.4               | 6.5     | 10.3    | 2.1     | 8.0     | 16.0   | Q1            |

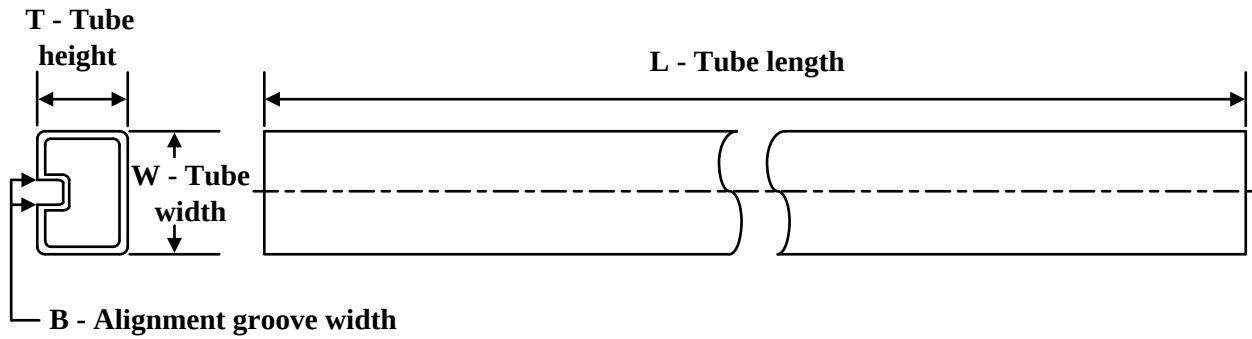
## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

| Device   | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|----------|--------------|-----------------|------|------|-------------|------------|-------------|
| TL598CDR | SOIC         | D               | 16   | 2500 | 353.0       | 353.0      | 32.0        |

## TUBE

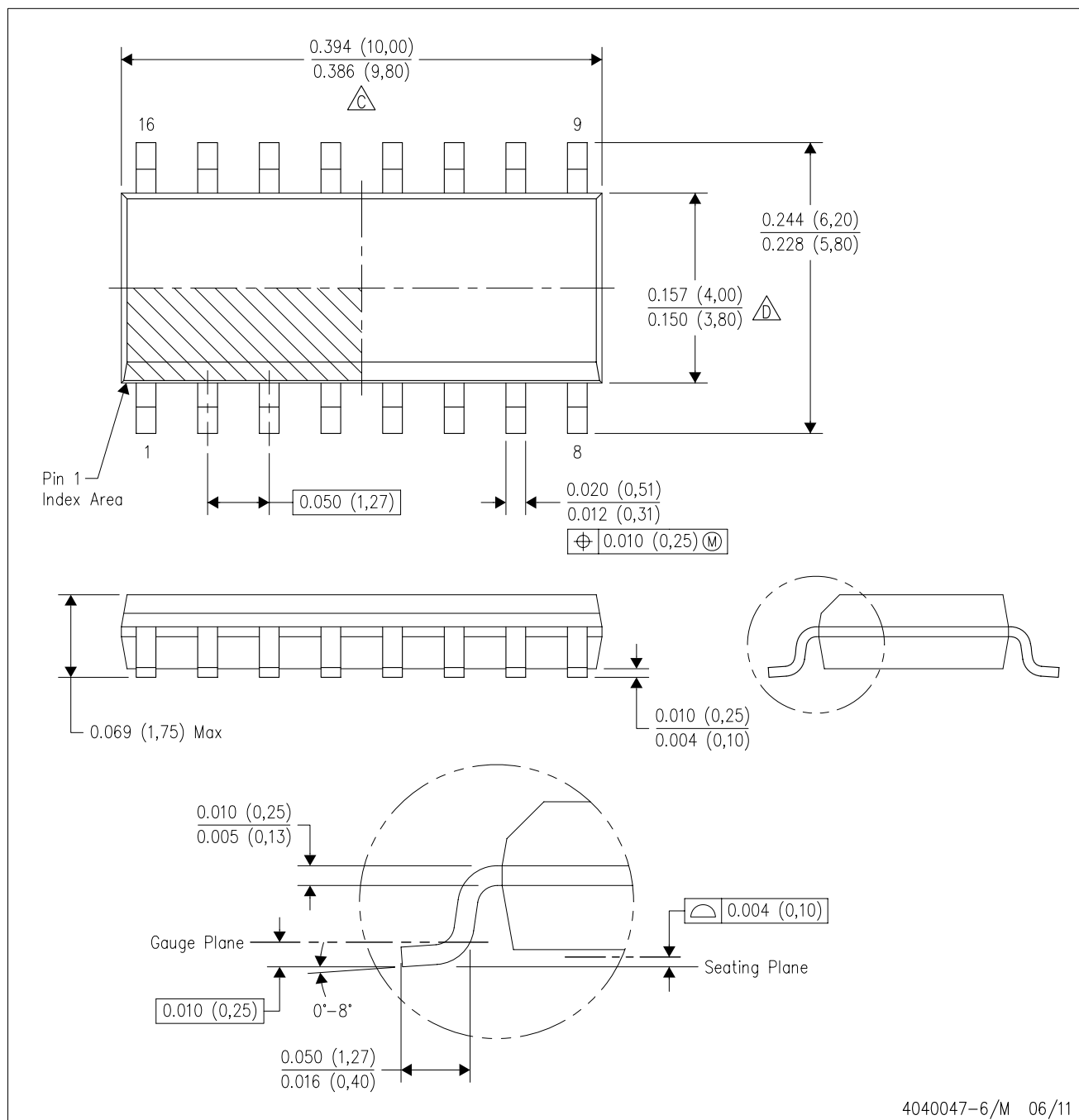


\*All dimensions are nominal

| Device    | Package Name | Package Type | Pins | SPQ | L (mm) | W (mm) | T (μm) | B (mm) |
|-----------|--------------|--------------|------|-----|--------|--------|--------|--------|
| TL598CN   | N            | PDIP         | 16   | 25  | 506    | 13.97  | 11230  | 4.32   |
| TL598CN.A | N            | PDIP         | 16   | 25  | 506    | 13.97  | 11230  | 4.32   |
| TL598CNE4 | N            | PDIP         | 16   | 25  | 506    | 13.97  | 11230  | 4.32   |

D (R-PDSO-G16)

PLASTIC SMALL OUTLINE

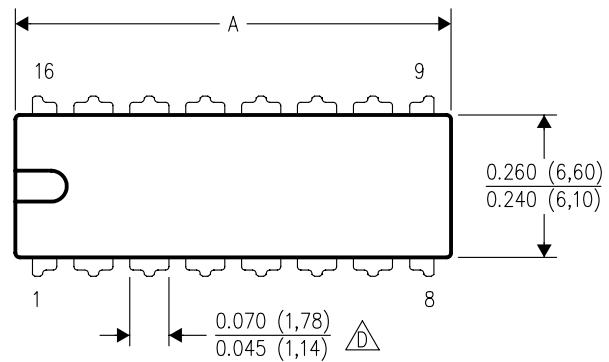


- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - $\triangle C$  Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
  - $\triangle D$  Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
  - E. Reference JEDEC MS-012 variation AC.

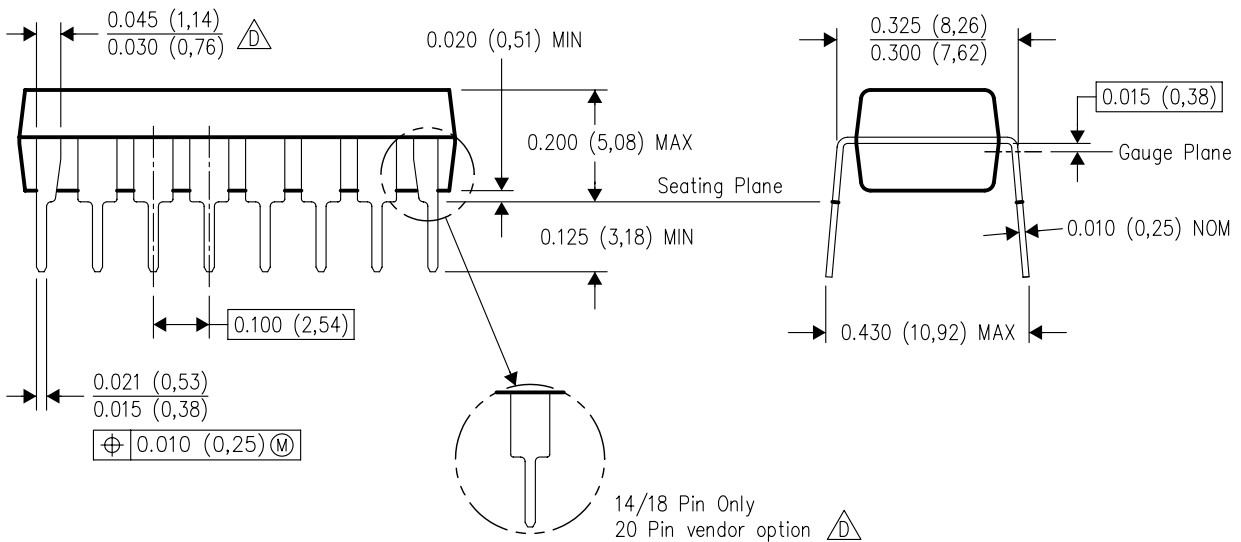
## N (R-PDIP-T\*\*)

16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



| PINS **             | 14               | 16               | 18               | 20               |
|---------------------|------------------|------------------|------------------|------------------|
| DIM                 |                  |                  |                  |                  |
| A MAX               | 0.775<br>(19,69) | 0.775<br>(19,69) | 0.920<br>(23,37) | 1.060<br>(26,92) |
| A MIN               | 0.745<br>(18,92) | 0.745<br>(18,92) | 0.850<br>(21,59) | 0.940<br>(23,88) |
| MS-001<br>VARIATION | AA               | BB               | AC               | AD               |



14/18 Pin Only  
20 Pin vendor option

4040049/E 12/2002

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
  - The 20 pin end lead shoulder width is a vendor option, either half or full width.

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